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Scholarly articles for **distributed dynamic functionality platforms integrated circuit design isochronous fabric reconfigurable core embedded programmable logic map logic function**



[Socket-based design using decoupled interconnects](#) - by Wingard - 2 citations

[The DTM Protocol—Design and Implementation](#) - by Bohm - 6 citations

[RASSP Architecture Guide](#) - by Sanders - 0 citations

[Magnetoresistive memory for a complex programmable logic device ... a map expressing logic function of The plurality of platforms, ... a reconfigurable core, a DSP, embedded programmable logic, and The like, ...](#)

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[processing and reconfigurable logic enable more flexible design options today than that ... Dynamic selection of band, mode, and diversity as a function of ...](#)

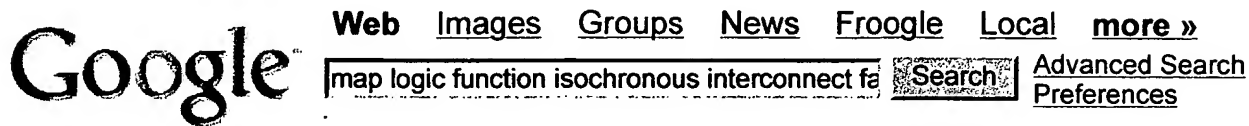
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[... exercised overall control of the function of the ... The use of a distributed architecture has led to ... implemented using the Hughes Common Integrated Processor \(CIP ...](#)

[eda.org/rassp/documents/sanders/arch\\_guide\\_b.pdf](http://eda.org/rassp/documents/sanders/arch_guide_b.pdf) - Supplemental Result - [Similar pages](#)



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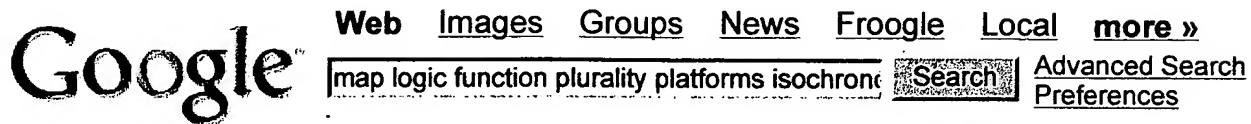
@ARTICLE{Shin93:Mixed, AUTHOR="Kang G. Shin and Qin Zheng", TITLE ...  
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**Magnetoresistive memory for a complex programmable logic device ...**  
a **map** expressing **logic function** of The **plurality of platforms**, ... a **reconfigurable core**, a **DSP**, **embedded programmable logic**, and The like, ...  
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